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ASA-1223

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: H. TANAKA

Serial No.: 10/522,712

Confirmation No.: 8479

Filed: January 28, 2005

For: SEMICONDUCTOR DEVICE

Group: 2111

Examiner: K. Kim

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 1, 2007

Sir:

In the matter of the above-identified application, Applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the foreign patent documents and non-patent references listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted after the mailing date of a notice of allowance but before payment of the issue fee and is accompanied by the fee of \$180.00 set forth in 37 CFR §1.17(p) and the following certification specified in 37 CFR §1.97(e):

On information and belief, I hereby certify that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

03/02/2007 SZEWDIE1 00000044 10522712

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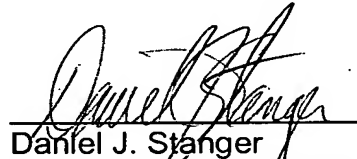
To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR §1.98(a)(3) for a concise explanation of the relevance is satisfied by an English language abstract of the document attached thereto.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Mattingly, Stanger, Malur & Brundidge, Deposit Account No. 50-1417 (referencing attorney docket no. ASA-1223) please credit any excess fees to such deposit account.

Respectfully submitted,

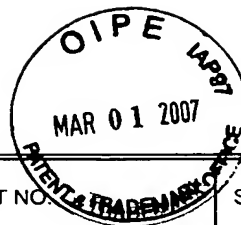
MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.



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**FORM PTO-1449** U.S. Department of  
Commerce (Rev. 4/92) Patent and Trademark  
Office

ATTY. DOCKET NO.

ASA-1223

SERIAL NO.

10/522,712

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

APPLICANT  
H. TANAKAFILING DATE  
January 28, 2005GROUP  
2111

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,752,035	5/1998	TRIMBERGER			

**FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT
					YES NO
EP 1 059 580 A1	12/2000	EUROPE			X

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	Tubella, et al., "Control speculation in multithreaded processors through dynamic loop detection", High Performance Computer Architecture 1998. Proceedings., 1998 Fourth International Symposium on Las Vegas, NV, USA, 1-4 Feb. 1998, Los Alamitos, CA, USA, IEEE Comput. Soc, US, 1 February 1998, pages 14-23, XP 010266834.
	Furber, "ARM System-on-Chip architecture", 2000, Addison-Wesley, London, XP002415715, pages 136-141.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.